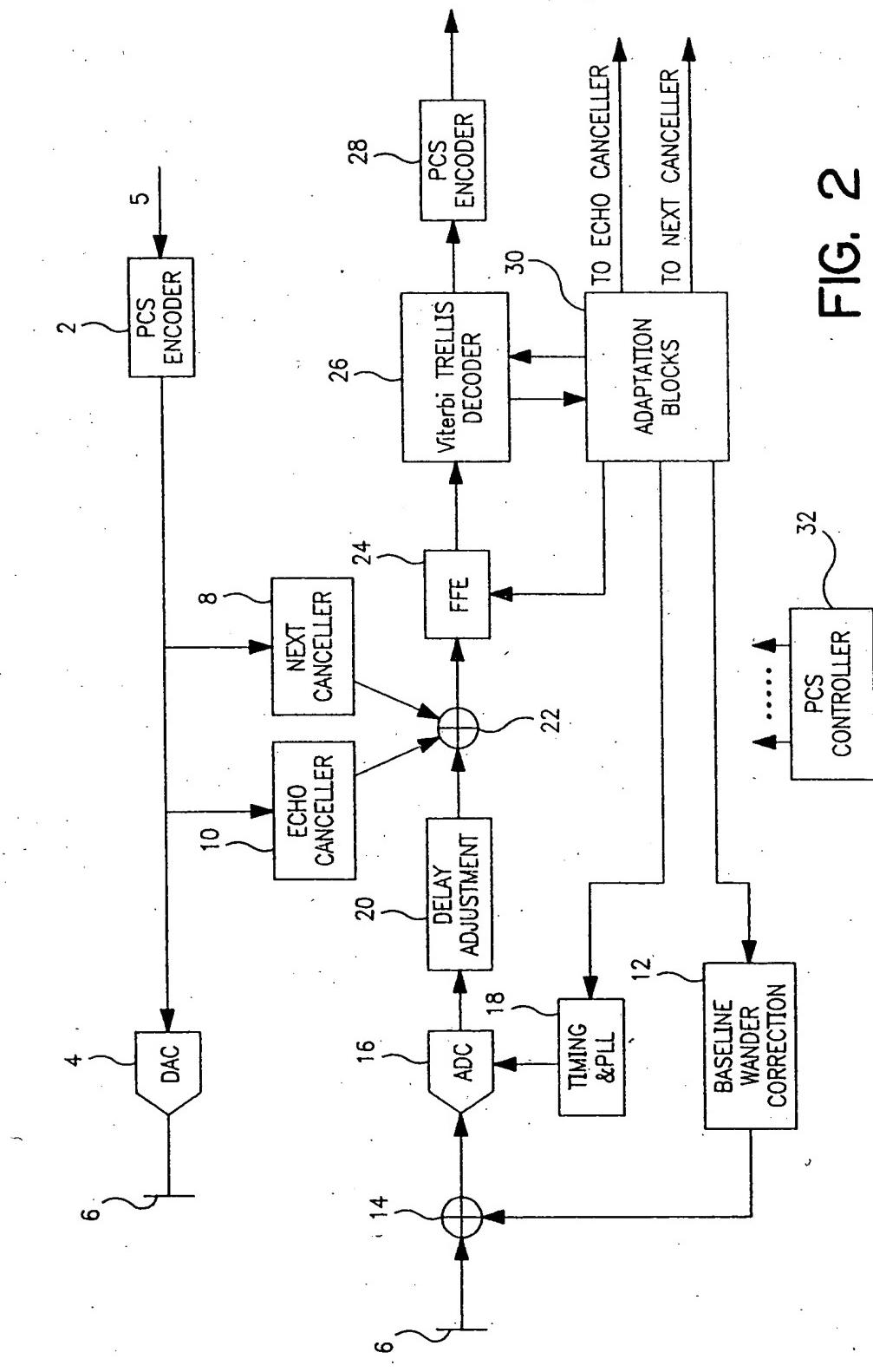


FIG. I

FIG. 2



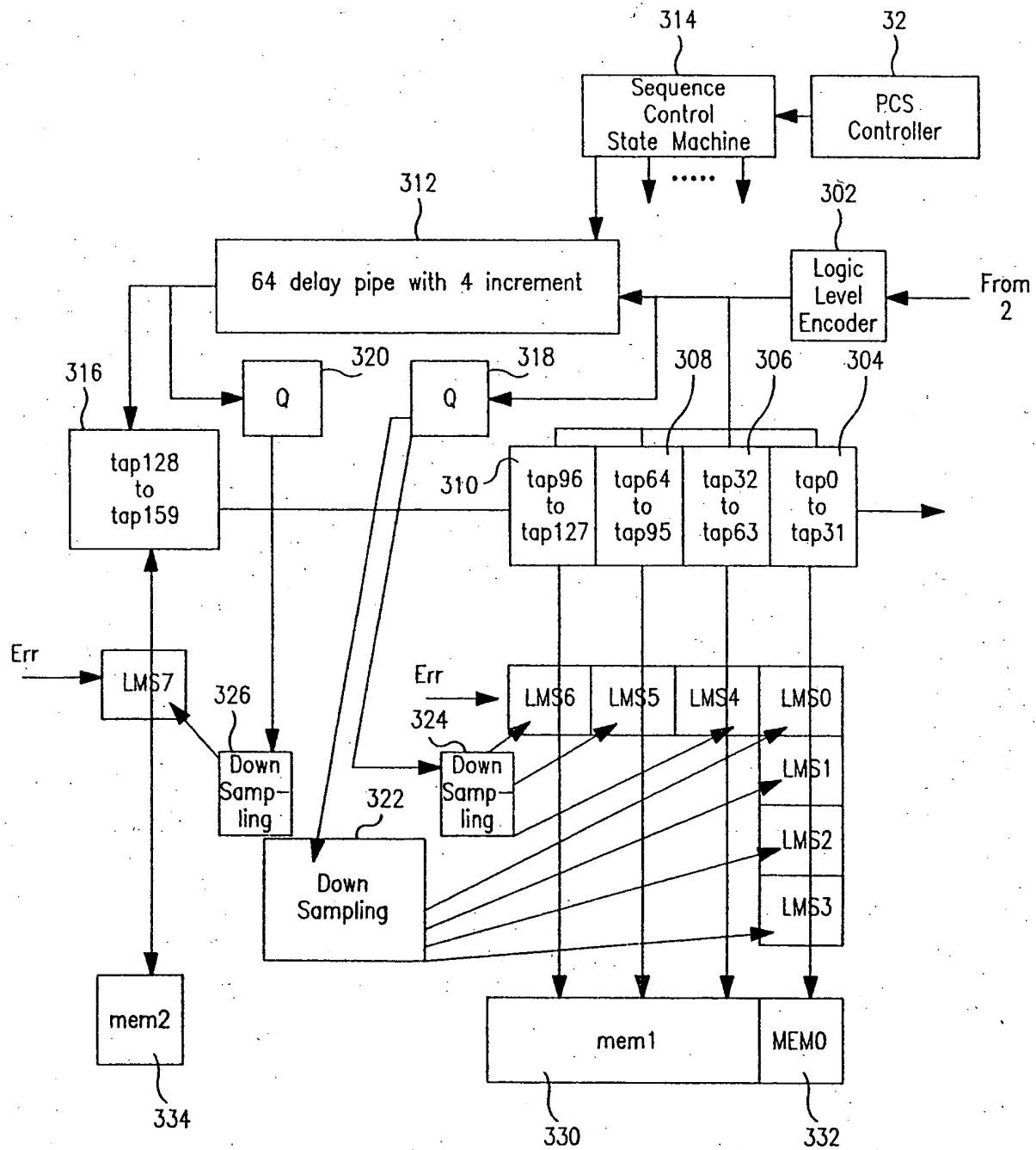


FIG. 3

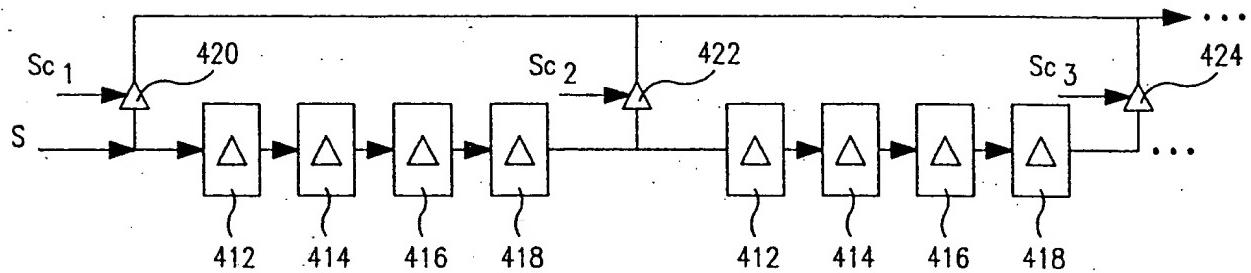


FIG. 4

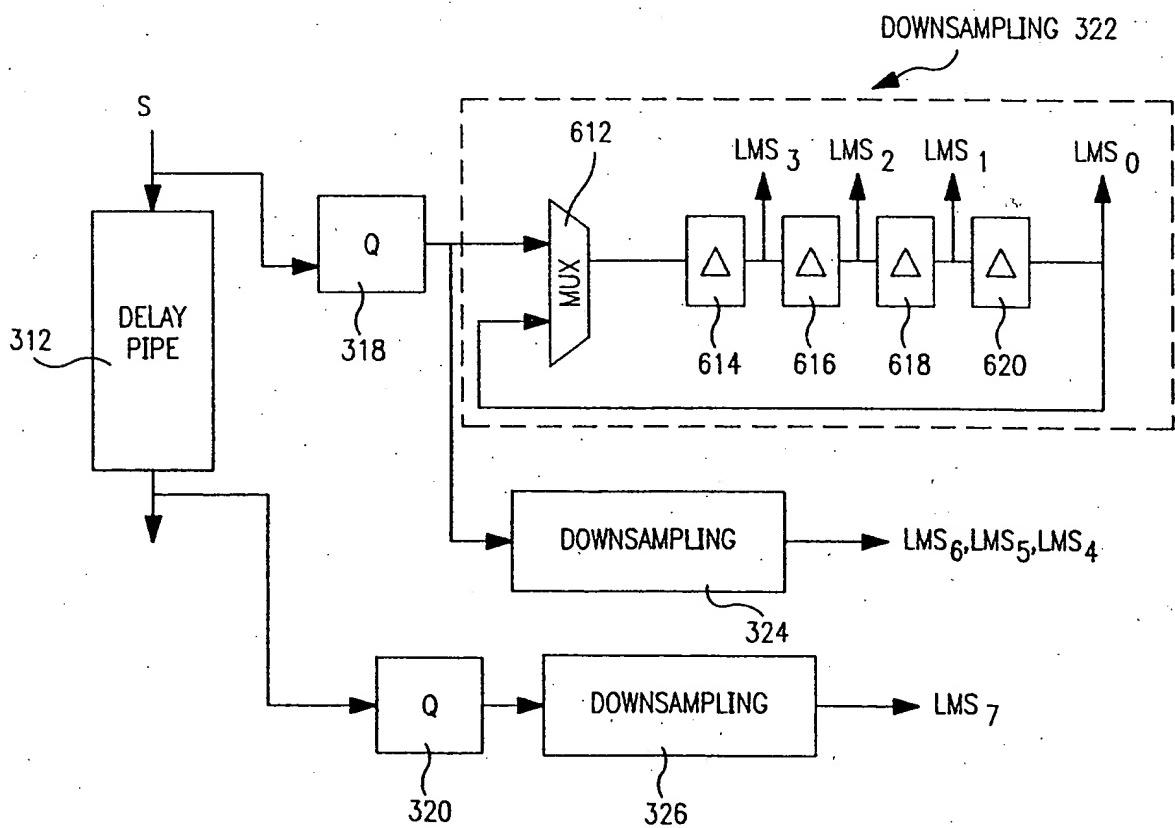


FIG. 6

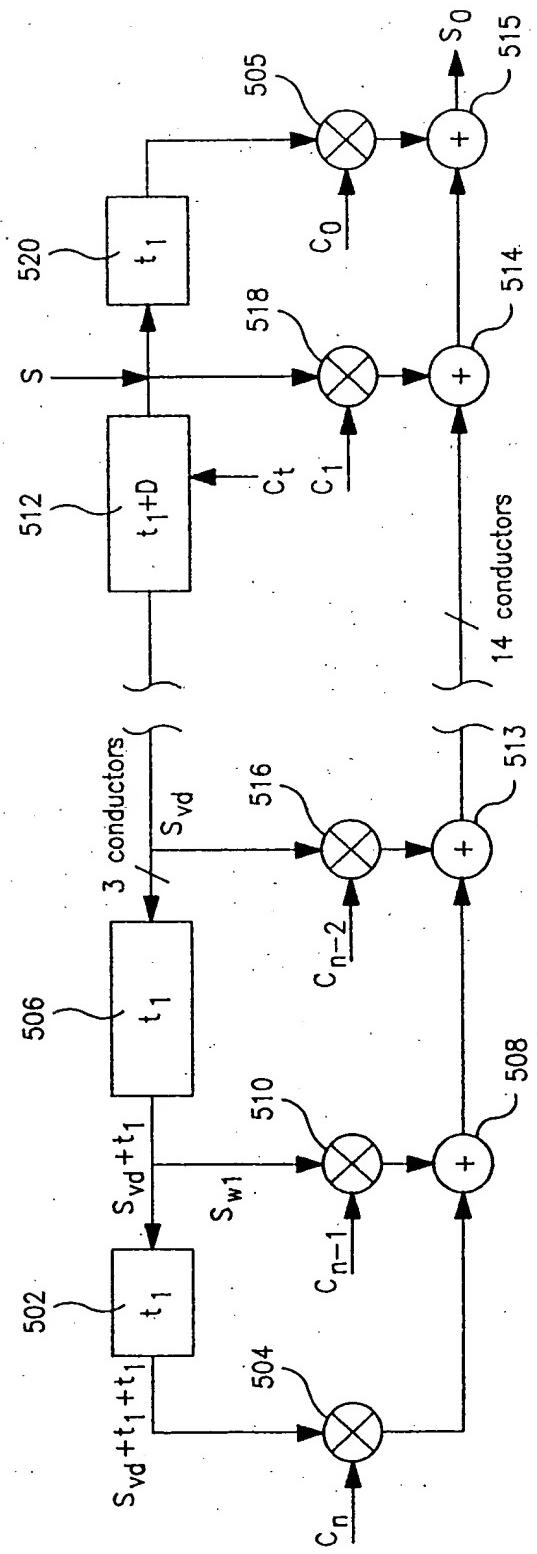


FIG. 5a

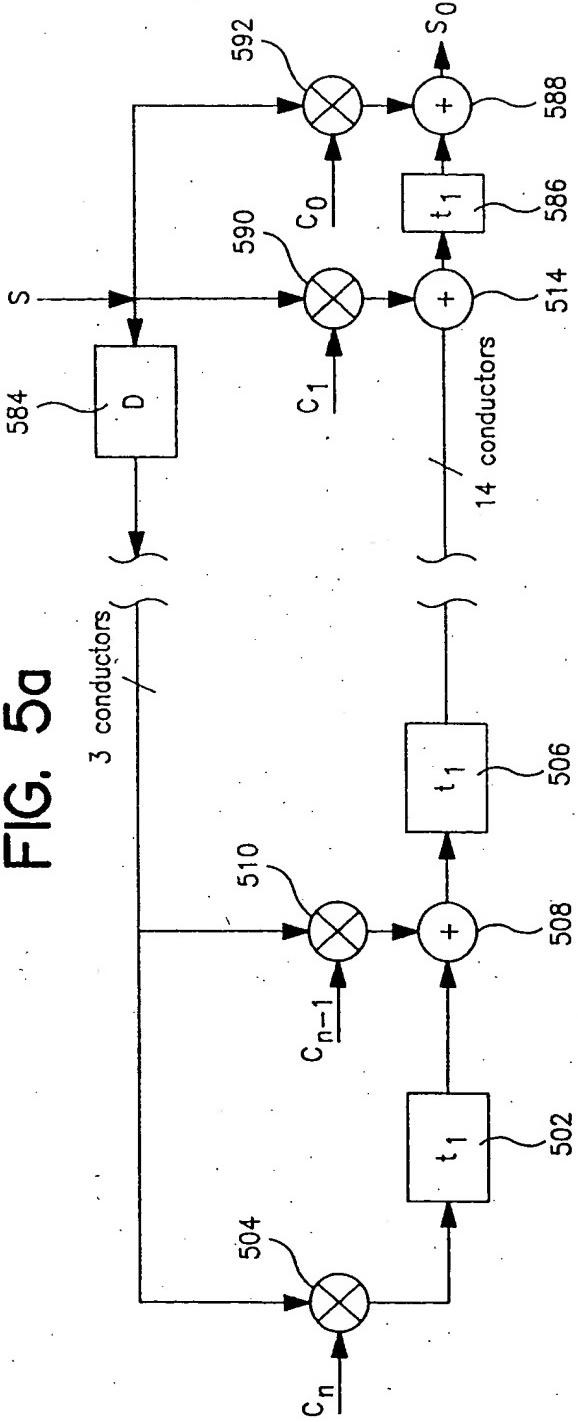


FIG. 5b

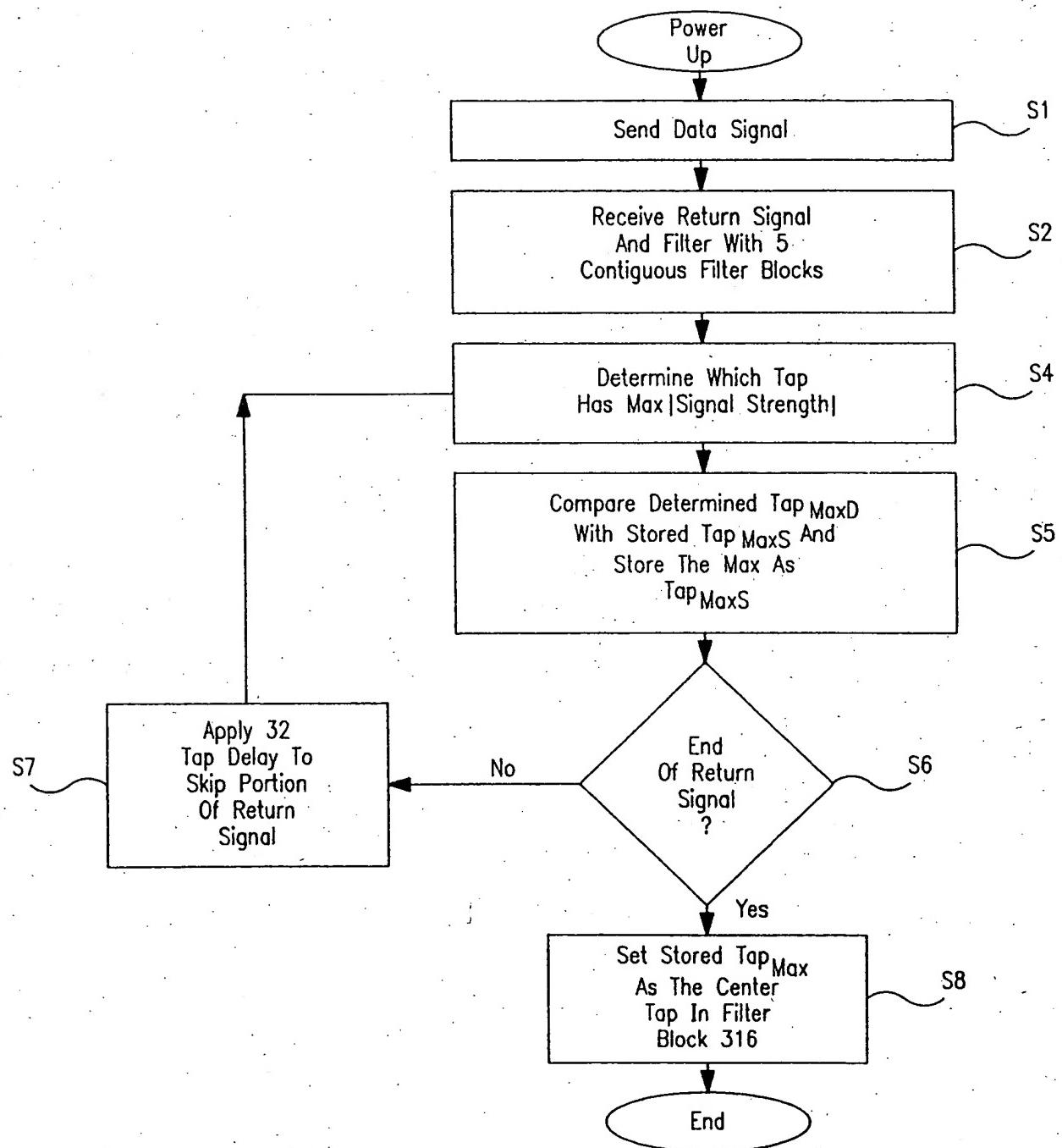


FIG. 7

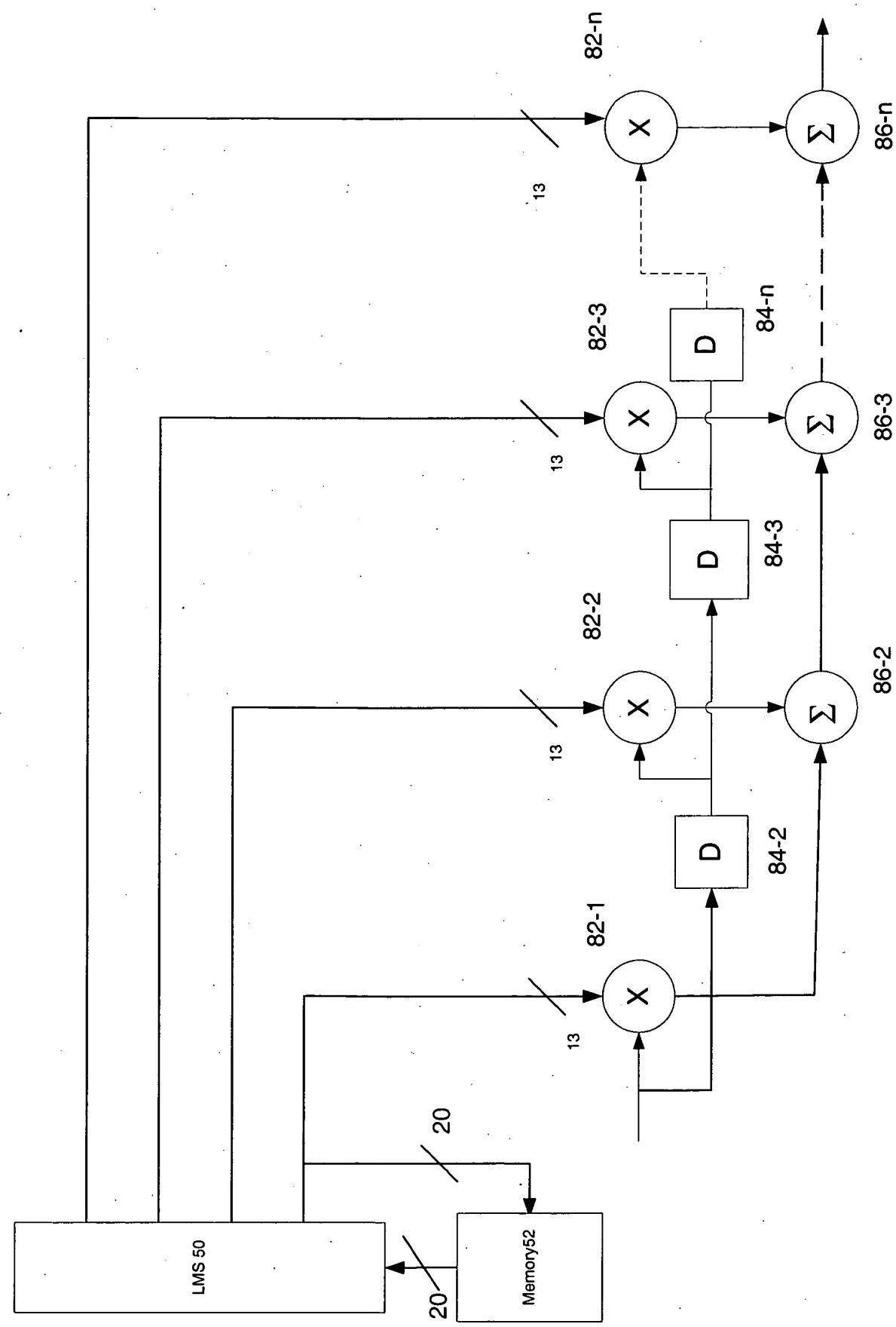


Fig. 8

Controller

55

Coefficients

LMS 50

20

Mémoire52

80-1

memory

80-2

memory

80-n

memory

82-n

82-3

memory

82-2

memory

82-1

memory

D

D

D

D

84-3

84-2

84-3

84-n

86-2

86-3

86-n

Fig. 9

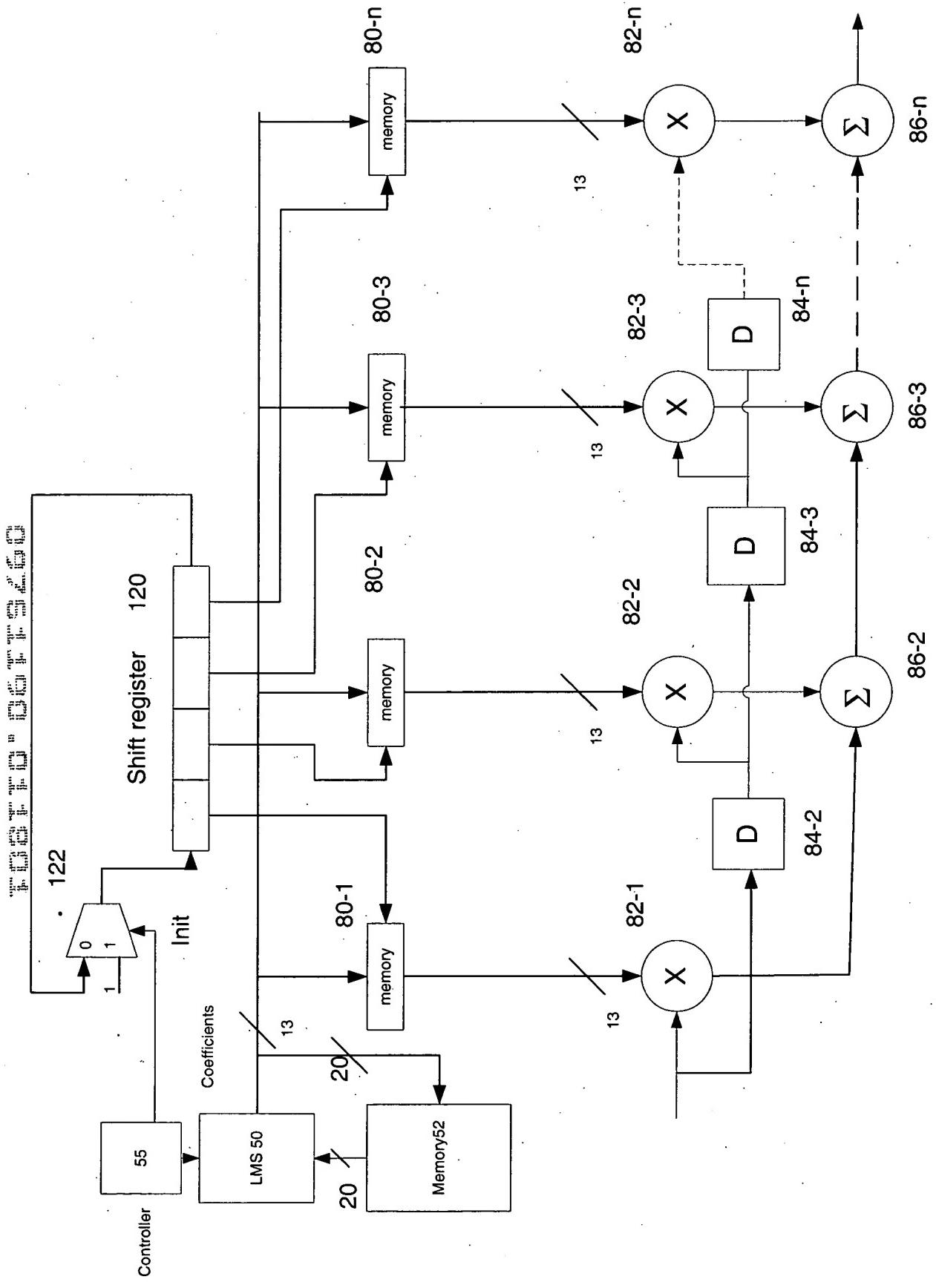


Fig. 10

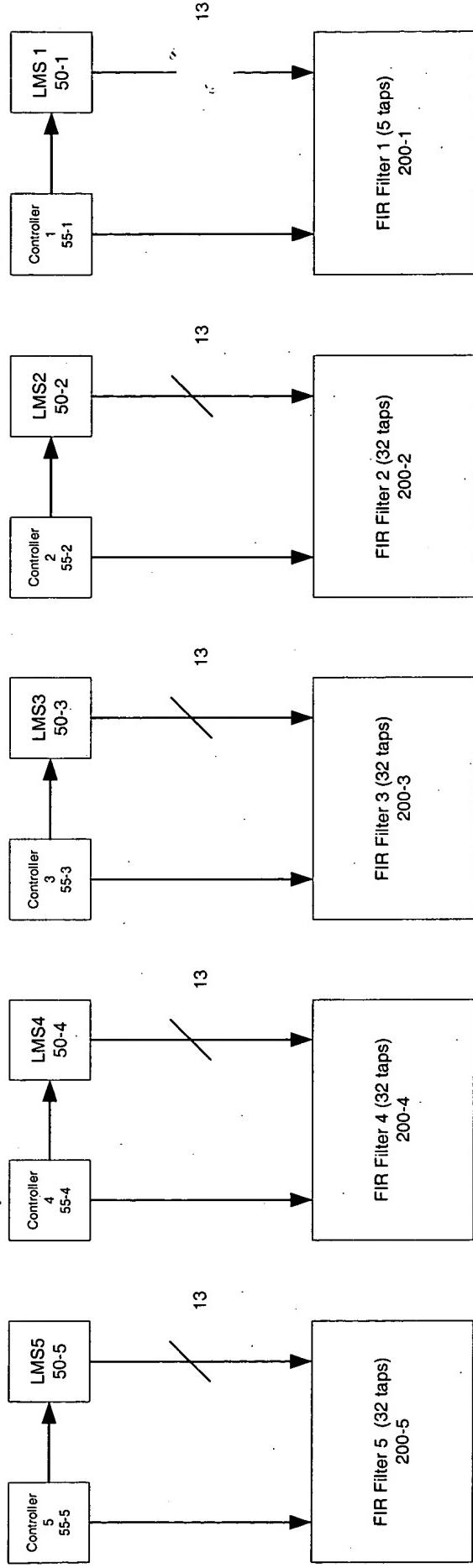


Fig
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